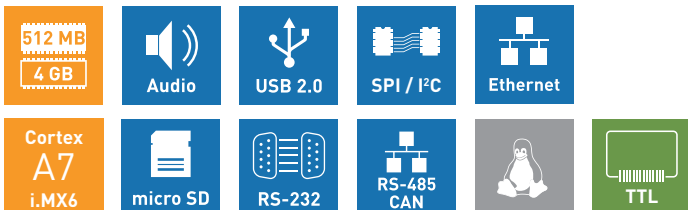
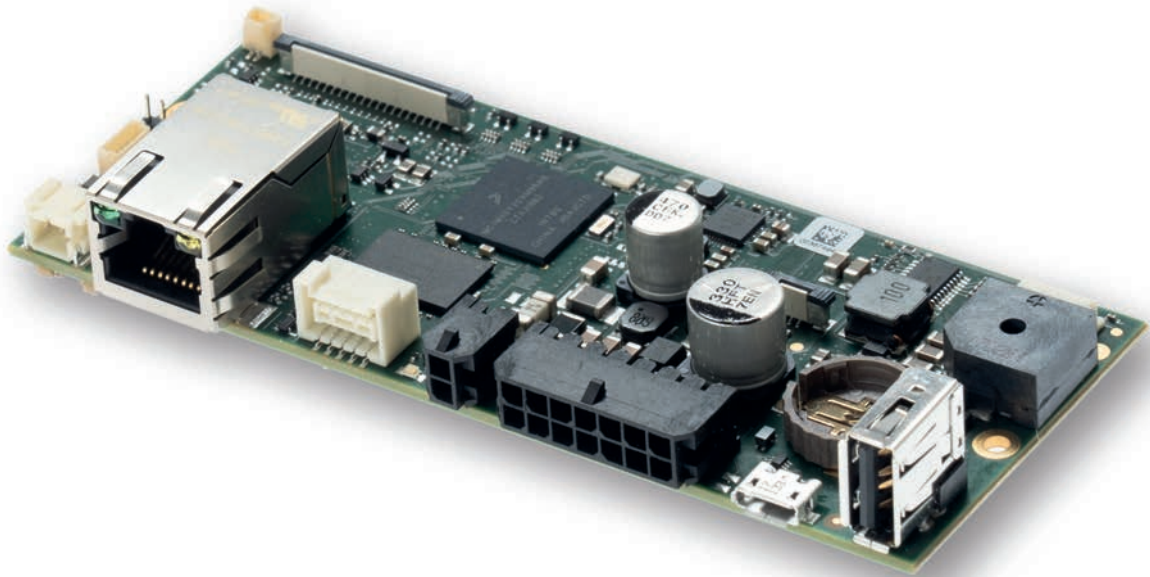


NALLINO core

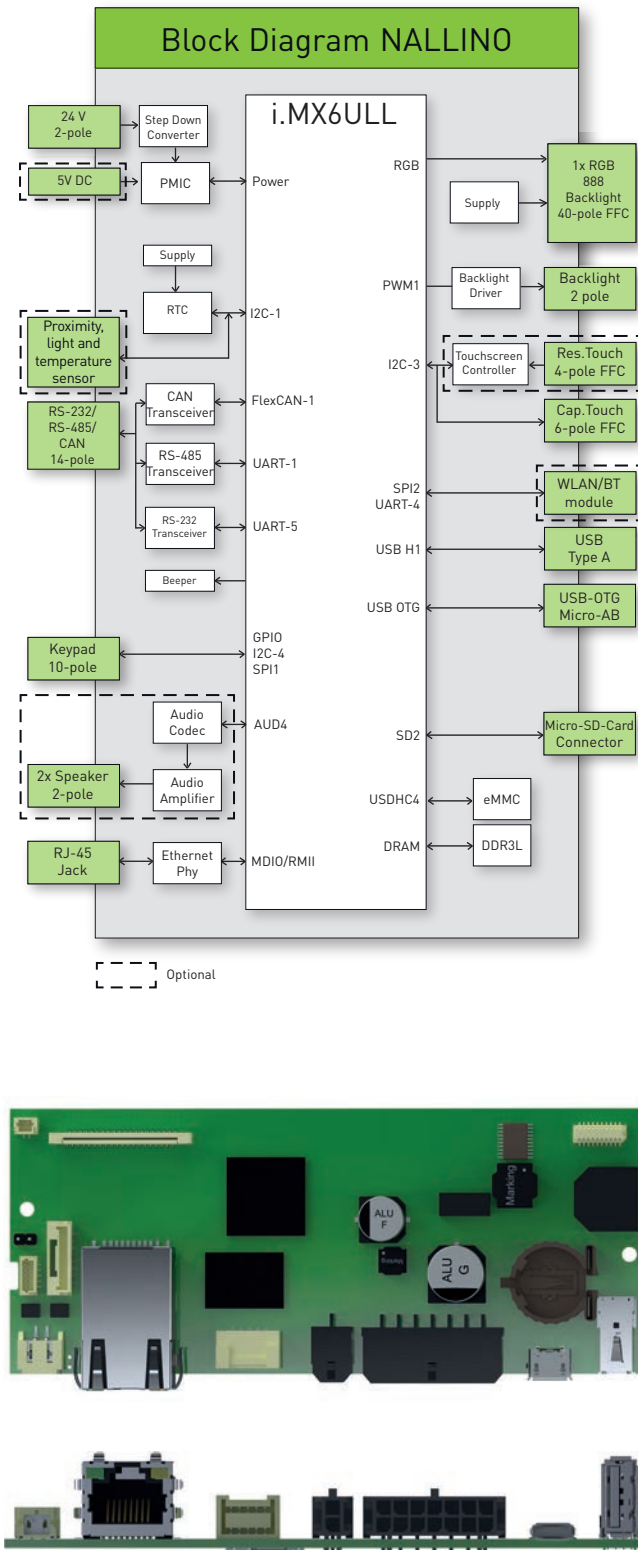
ARM Cortex-A7 Single Board Computer



Online support at www.garz-fricke.com

PRELIMINARY TECHNICAL SPECIFICATION

NALLINO Single Board Computer



CPU	Standard	M
CPU Type	i.MX6ULL	
Core Class	ARM Cortex - A7	
Core Clock	800 MHz	
Features	NEON for SIMD media acceleration and VFP operations; Display support up to WXGA (1366 x768) at 60 Hz; L1 cache, 32 KB for instruction and 32 KB for data; 128 KB L2 cache	
HW Accelerators	Pixel Processing Pipeline for image resize, rotation, overlay and CSC	
RTC	Accuracy: +/- 30 ppm at 25°C	
Memory		
eMMC Flash	4 GB MLC eMMC	
RAM Standard	512 MB 16 bit DDR3L	
Micro SD Card Slot	4 bit MMC/SDIO/SD/SDHC	
Operating Systems		
Supported OS	Linux Yocto	
Communication Interfaces		
Network	1x 10/100 Mbit/s Ethernet (RJ-45), WiFi on request	
RS-485	1x RS-485 (Half duplex) & 1x CAN (ISO/DIS 11898)	
RS-232	1x RS-232 (RX/TX/CTS/RTS)	
USB 2.0	1x 480 Mbit/s Host (Type A) 1x 480 Mbit/s OTG (Type Micro-AB)	1x 480 Mbit/s OTG (Type Micro-AB)
Synchronous Serial Interface	SPI, I²C, Matrix Keypad up to 4 x 4	
Audio		
Audio Internal	Buzzer	
Audio Output	1x Speaker connector, 1.5 W RMS (80hm)	
Display and Touch		
Display Interface	TTL, 24 bit(RGB)	
Touch Interface	PCAP I²C (4-wire analog resistive, optional)	
Backlight	Integrated backlight driver (111 mA driving current)	
Device Dimensions		
W x H x D	113.8 x 18.0 x 47.3 mm	
Weight	50 g	
Power Supply		
Supply Voltage	Nom. 9 to 32 V DC	Regulated 5 V DC
Consumption	tbd.	tbd.
Typical Environmental Conditions		
Storage Temp.	-20 to +70 °C	
Operating Temp.	tbd.	
Humidity	5 to 90 % RH, non-condensing	

"THIS PRODUCT CAN BE MODIFIED BY ALTERNATIVE ASSEMBLY. PLEASE CONTACT US FOR AVAILABLE OPTIONS."